Tagore Technology

TL0374J

0.03 – 3.0 GHz GaAs Ultra Low Noise Amplifier

Application Note: TL0374J EVB D2

Application Note

30MHz~2600MHz

5V 55mA

Rev-1.2

List of Contents

1. General Description

The TL0374J is a broadband, ultra-low Noise Amplifier (LNA) providing high gain and linearity. With a simple input and output match, this LNA can be tuned for different frequency bands targeting LTE (small cells and infrastructure) and any other applications requiring low noise, high gain, and linearity. For >3GHz frequency band, TL0375J can be considered. The TL0374J is packaged in a compact, low-cost Dual Flat No Lead (DFN) 2x2x0.75mm, 8 pin plastic package.
TL0374J-EVB-D2 is an evaluation board specially tuned for 5V 55mA for frequency range of 30MHz~2600MHz applications. Its high gain, low noise performance makes it suitable.

2. TL0374J-EVB-D2 Board Details

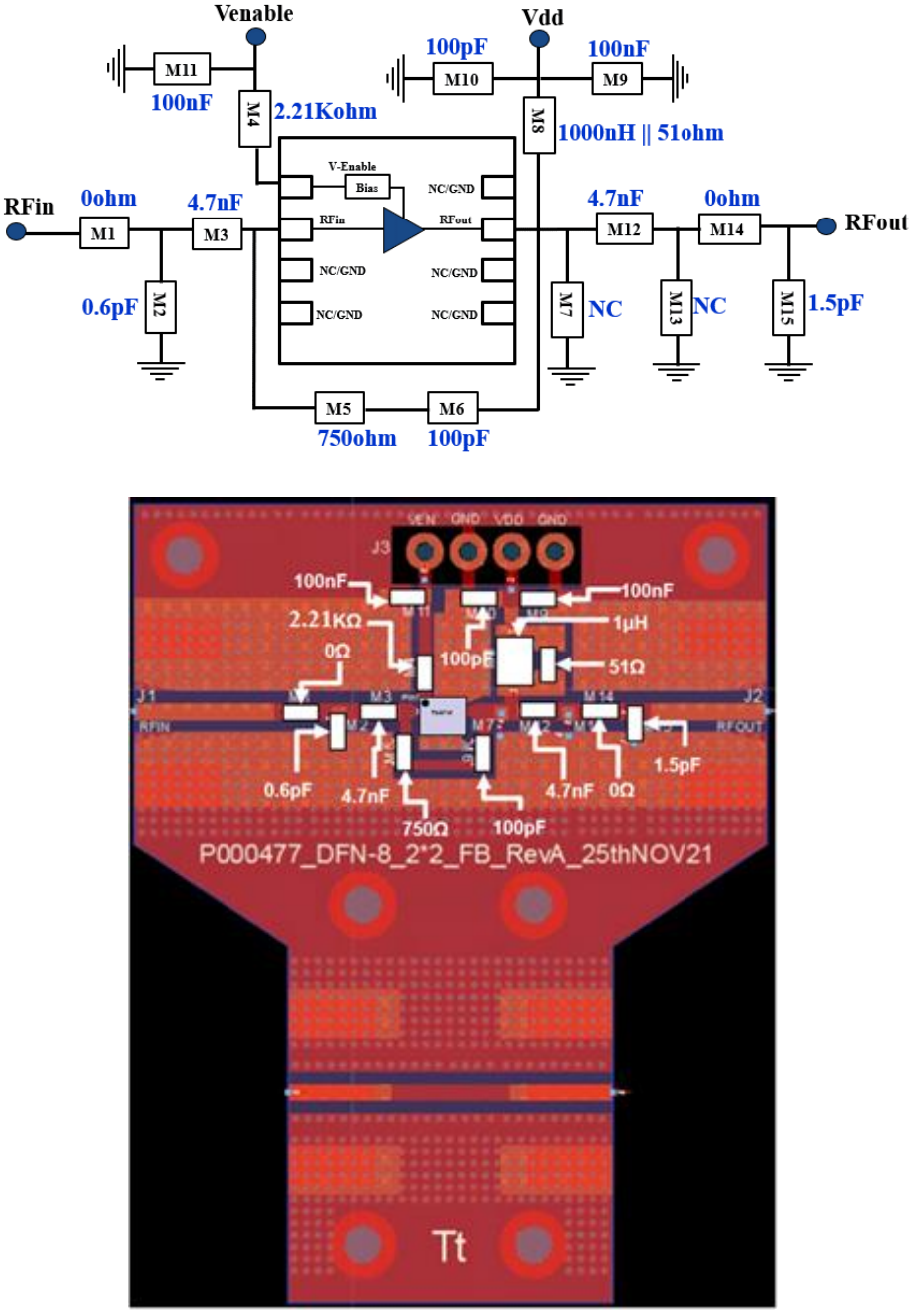


Figure 2.1 TL0374J-EVB-D2 30MHz ~ 2600MHz Schematic and EVB Layout

3. TL0374J-EVB-D2 Bill of Material

Component ID	Value	Manufacturer	Recommended Part Number
M1, M14	0Ω	Panasonic	ERJ-2GE0R00X
M2	0.6pF	Murata	GJM1555C1HR60BB01D
M3, M12	4.7nF, 50V	Murata	GRM1885C1H472JA01D
M4	2.21KΩ	Panasonic	ERJ-2RKF2211X
M5	750Ω	KOA Speer	RK73H1ERTTP7500F
M6, M10	100pF	AVX	04025A101JAT4A
M8	1µH	Coil craft	PFL2512-102MEC
M8	51Ω	R0HM Semiconductor	ESR03EZPJ510
M9, M11	100nF	TDK	C1005X7R1H104K050BE
M15	1.5pF	Murata	GJM1555C1H1R5BB01J
Q1	GaAs LNA	Tagore Technology	TL0374J
PCB		Rogers RO4350B, 20 mils, 1 oz copper	

Table 3.1 TL0374J-EVB-D2 BOM

4. TL0374J-EVB-D2 Biasing Sequence

Turn ON Device	Turn OFF Device
1. Set Venable to +5V 2. Set V_{DD} to +5V 3. Device will draw required I_{DQ} current 4. Apply RF power	1. Turn RF power off 2. Turn off V_{DD} 3. Turn off Venable

Table 4.1 TL0374J-EVB-D2 Bias and Sequencing

5. TL0374J-EVB-D2 Board Measurement Summary

Frequency (MHz)	De-embedded Noise figure (dB)	Gain(dB)	OP1 (dBm)	OIP3(dBm) Fspacing:1MHz 0dBm Pout/tone	S11(dB)	S22(dB)	Mu1
30	0.9	21.9	13.8	28.6	-17.5	-6.4	1.1
100	0.7	21.9	14.6	27.2	-23.5	-6.8	1.1
250	0.7	21.5	14.8	28.4	-22.0	-7.8	1.2
500	0.7	20.4	14.3	28.4	-16.6	-11.9	1.4
1000	0.7	17.1	14.4	28.5	-11.2	-17.4	2.4
1500	0.8	14.9	15.6	29.9	-8.9	-9.3	1.9
2000	0.9	13.5	16.5	31.3	-8.9	-7.9	1.7
2600	0.9	14.0	17.8	32.8	-16.1	-19.6	2.0

Table 5.1 TL0374J-EVB-D2 Electrical Characteristics Summary

6. TL0374J-EVB-D2 Test Results

All the tests are carried out at room temperature.

6.1. S parameters

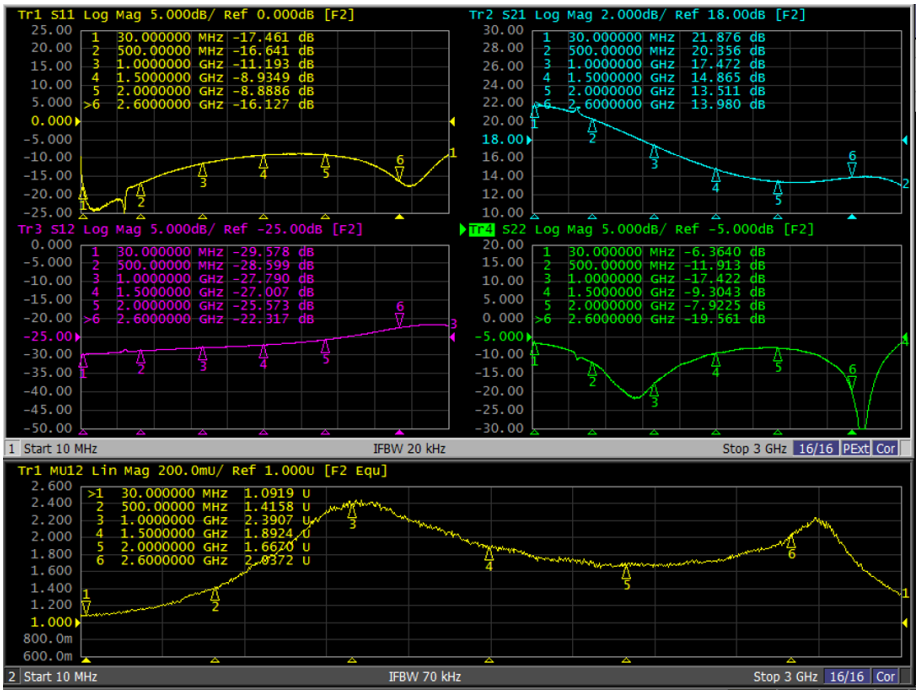
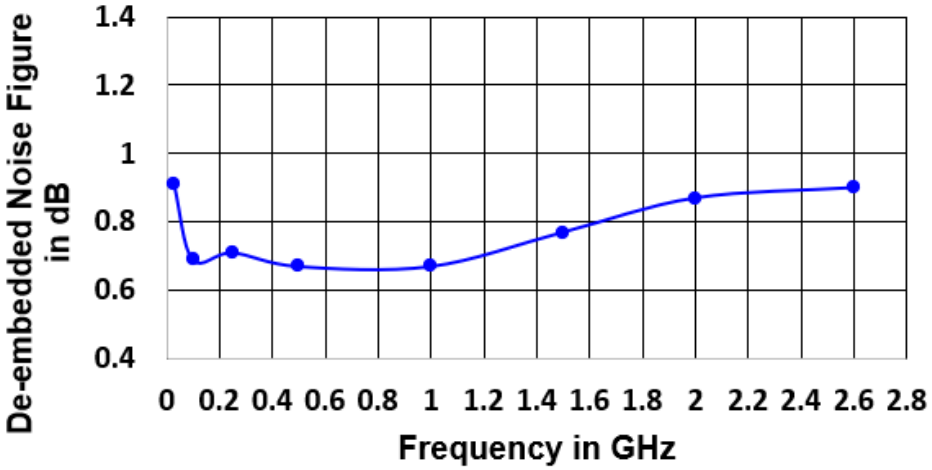


Figure 6.1.1. S parameters of TL0374J-EVB-D2

6.2. De-embedded Noise Figure



**** Note:** Trace loss is around 0.02-0.1dB. So SMA-SMA NF will lie between 0.7dB to 1.0dB.

Figure 6.2.1. De-embedded Noise Figure of TL0374J-EVB-D2

6.3. Large Signal Test Results

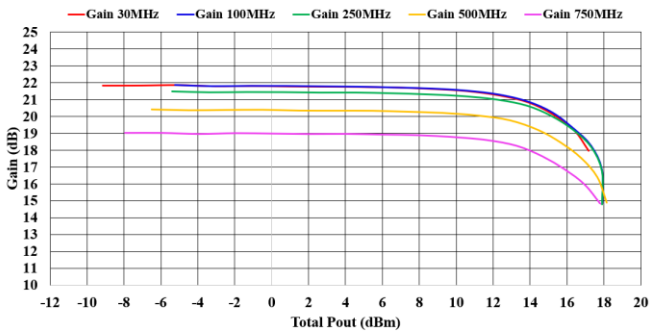


Figure 6.3.1. Gain Vs Pout of TL0374J-EVB-D2[30M-750MHz]

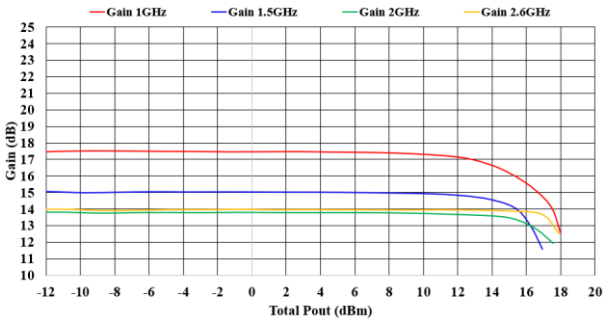


Figure 6.3.2. Gain Vs Pout of TL0374J-EVB-D2[1G-2.6GHz]

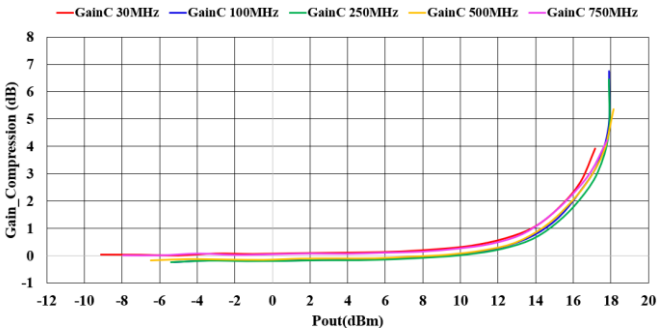


Figure 6.3.3. Gain compression Vs Pout of TL0374J-EVB-D2[30M-750MHz]

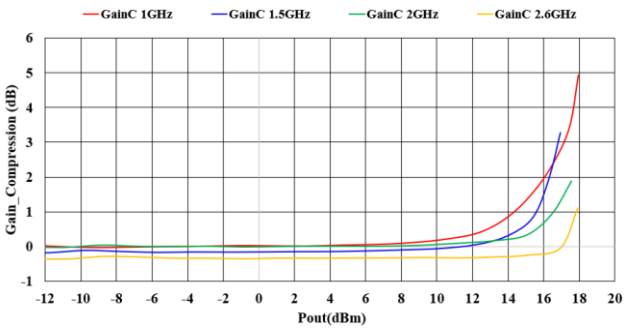


Figure 6.3.4. Gain compression Vs Pout of TL0374J-EVB-D2[1G-2.6GHz]

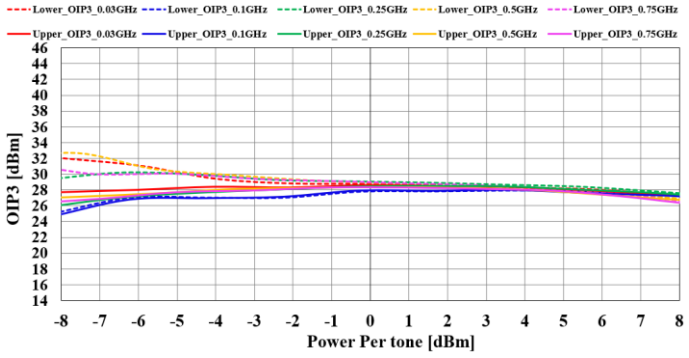


Figure 6.3.5. Output 3rd Order Intercept Point of TL0374J-EVB-D2[30M-750MHz]

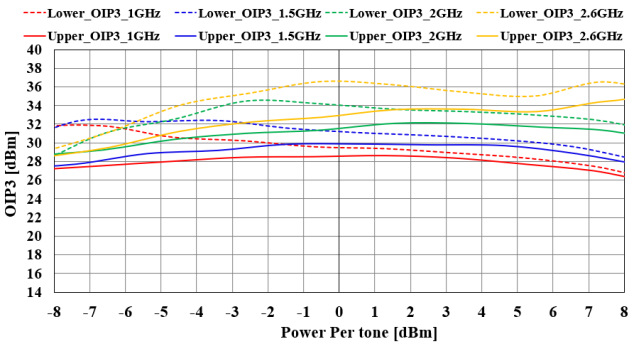


Figure 6.3.6. Output 3rd Order Intercept Point of TL0374J-EVB-D2[1G-2.6GHz]

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