# TL0374J LNA Evaluation Board, 30 – 1000 MHz, 3.3 V, 30 mA

## General Description

The TL0374J is a broadband, ultra-low noise amplifier (LNA) providing high gain and linearity. With a simple input and output match, the LNA can be tuned for different frequency bands targeting LTE (small cells and infrastructure) and any other applications requiring low noise, high gain, and linearity. For frequency bands above 3 GHz, the TagoreTech TL0375J can be considered. The TL0374J is housed in a compact, low-cost 2.0 $\times$ 2.0 mm$^2$ 8-lead Dual Flat No-lead (DFN) plastic package.

TL0374J-EVB-C is an evaluation board tuned for the 30 – 1000 MHz band at 3.3 V, 30 mA. This application note gives the schematic, bill of materials, biasing sequence and test set-up of the board, and its measured S-parameters, noise figure, gain compression and third-order intercept point.

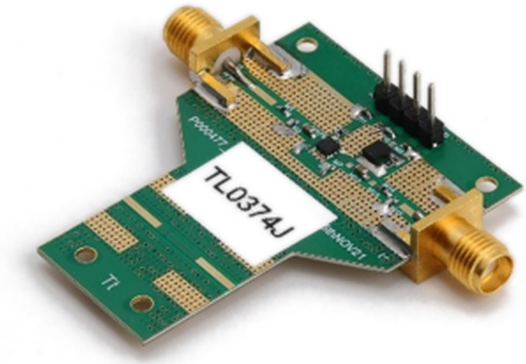



Figure 1: TL0374J evaluation board with the resistive-feedback layout used on TL0374J-EVB-C.

## Evaluation Board Highlights

- Frequency range 30 MHz to 1000 MHz
- $V_{DD} = V_{enable} = 3.3$ V, $I_{DQ} = 30$ mA
- Gain: 21.3 – 25.7 dB
- Noise figure (de-embedded): 0.5 – 1.1 dB
- $OP_{1dB}$: 14.3 – 15.2 dBm
- OIP3: 27.5 – 29.3 dBm (0 dBm per tone, 1 MHz spacing)
- Input return loss ($S_{11}$): -26.3 to -12.5 dB

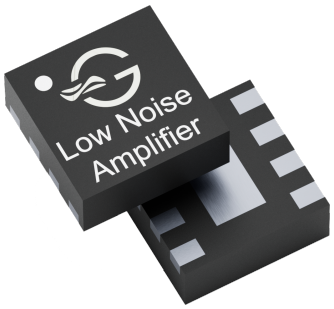



Figure 2: TL0374J in 2.0 x 2.0 mm$^2$ DFN 8-pin package.

## Related Documents

TL0374J datasheet: electrical specifications, package information and all evaluation boards.

Please refer to application notes TN-001 PCB Design guideline and TN-002 Solder Rework at TagoreTech web page for PCB and soldering guidelines.

RoHS

RoHS10/REACH/Halogen free

# Contents

**General Description** 1
**Evaluation Board Highlights** 1
**Related Documents** 1
**Ordering Information** 3
**TL0374J-EVB-C Board Details** 3
**TL0374J-EVB-C Bill of Materials** 4
**TL0374J-EVB-C Biasing Sequence** 4
**Test Set-up** 5
**TL0374J-EVB-C Measurement Summary** 6
**TL0374J-EVB-C Measurement Results** 7
S-parameters, 30 – 1000 MHz band . . . 8
Noise figure and large-signal performance . . . 9
**Changelog** 9
**Legal Information** 10
Published by . . . 10
Legal Disclaimer . . . 10
Sales Contact . . . 10
Buy TL0374J directly . . . 10
TL0374J Product information . . . 10
TagoreTech Inc. Product Brochure and datasheets . . . 10


©2024-2026 TagoreTech Inc.

## Ordering Information

Table 1: Ordering information

| Part Number | Description | Notes |
|---|---|---|
| TL0374J-EVB-C | Evaluation board | Tuned, 30 – 1000 MHz, 3.3 V, 30 mA |
| TL0374J | 8 Pin 2.0x2.0x0.75 $mm^3$ DFN | Core part number |

## TL0374J-EVB-C Board Details

The board uses a Rogers RO4350B, 20 mil, 1 oz copper PCB with SMA connectors on $RF_{IN}$ and $RF_{OUT}$ and a header for $V_{enable}$, $V_{DD}$ and ground. The board uses resistive feedback: M5 with the DC-blocking capacitor M6, connected from $RF_{OUT}$ to $RF_{IN}$, flattens the gain and gives a broadband input and output match. M1 is the input DC block; the output is matched with M12 and M14. The drain is fed through M8, an inductor in parallel with a resistor that damps the choke resonance, and decoupled by M9 and M10. The series resistor M4 on the $V_{enable}$ pin sets $I_{DQ}$, M11 decouples $V_{enable}$ (see the TL0374J datasheet for the bias resistor curve). Component positions that are not assembled are shown dashed grey in the schematic and marked DNP (Do Not Populate) in the bill of materials.

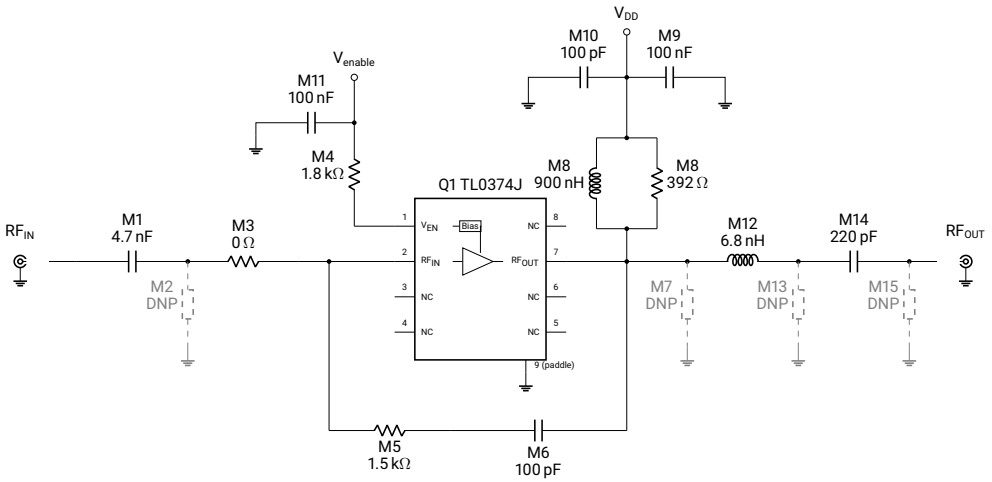



Figure 3: Schematic of TL0374J-EVB-C, 30 – 1000 MHz. Dashed grey positions are not assembled (DNP).

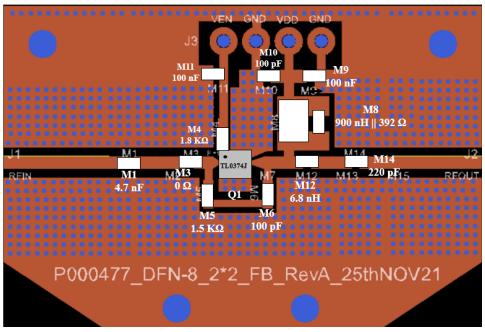



Figure 4: Layout of TL0374J-EVB-C.


©2024-2026 TagoreTech Inc.

# TL0374J-EVB-C Bill of Materials

Table 2: Bill of materials, TL0374J-EVB-C

| Component ID | Value | Manufacturer | Recommended part number |
|---|---|---|---|
| M1 | 4.7 nF, 50 V | Murata | GRM1885C1H472JA01D |
| M3 | 0 Ω | Panasonic | ERJ-2GE0R00X |
| M4 | 1.8 kΩ | Panasonic | ERJ-2RKF1801X |
| M5 | 1.5 kΩ | Panasonic | ERJ-2RKF1501X |
| M6, M10 | 100 pF | AVX | 04025A101JAT4A |
| M8 (L) | 900 nH | Coilcraft | 1008AF-901XJLC |
| M8 (R) | 392 Ω | Panasonic | ERJ-UP3F3920V |
| M9, M11 | 100 nF | TDK | C1005X7R1H104K050BE |
| M12 | 6.8 nH | Coilcraft | 0402HP-6N8XJRW |
| M14 | 220 pF | Kemet | C0402C221K5GACAUTO |
| M2, M7, M13, M15 | DNP | | |
| Q1 | GaAs LNA | TagoreTech | TL0374J |
| PCB | Rogers RO4350B, 20 mil, 1 oz copper | | |

# TL0374J-EVB-C Biasing Sequence

TagoreTech recommends following the biasing sequence below every time the evaluation board is powered up or down. Applying RF power to an amplifier that is not yet biased, removing the bias while RF power is still present, or letting a supply overshoot during switching can stress the input transistor and damage the device or reduce its long-term reliability. The sequence is part of the board's normal operating procedure, not an optional step.

Before switching on, set the current limit of the supply to about 70 mA, the absolute maximum drain current, and make sure that the supply does not overshoot $V_{DD}$ or $V_{enable}$ above the +6 V absolute maximum rating when it is switched on. The RF input power must stay below the absolute maximum of 23 dBm CW. The TL0374J is ESD sensitive (HBM class 1B); handle the board with ESD precautions and connect the ground before any other connection.

Table 3: TL0374J-EVB-C bias and sequencing

| Turn on | Turn off |
|---|---|
| 1. Connect the supply ground to the ground test point. | 1. Turn the RF power off. |
| 2. Set $V_{enable}$ to +3.3 V. | 2. Turn $V_{DD}$ off. |
| 3. Set $V_{DD}$ to +3.3 V and check that the quiescent current is $I_{DQ} \approx 30$ mA. | 3. Turn $V_{enable}$ off. |
| 4. Apply the RF power. | 4. Disconnect the ground last. |

$I_{DQ}$ is set by $V_{enable}$ and the series resistor M4; a quiescent current clearly different from 30 mA indicates a wrong supply voltage, a wrong M4 value or an assembly problem, and the RF power should not be applied before the cause is found. $V_{enable} < 0.2$ V disables the device. When $V_{enable}$ and $V_{DD}$ are supplied from the same 3.3 V source, as in the test set-up of Figure 5, they rise and fall together; the RF power must still be applied only after the supply has settled and removed before the supply is switched off.


©2024-2026 TagoreTech Inc.

## Test Set-up

The TL0374J-EVB-C is shipped fully assembled and tested. Figure 5 shows the basic test set-up for evaluating the S-parameters (gain, input and output return loss, reverse isolation) with a vector network analyzer.

1. Connect the ground test point to the ground terminal of the power supply.
2. Connect the $V_{enable}$ and $V_{DD}$ test points to the output of a 3.3 V supply capable of sourcing approximately 30 mA.
3. Connect a calibrated network analyzer to the $RF_{IN}$ and $RF_{OUT}$ SMA connectors. Sweep the frequency from 10 MHz to 3 GHz with a source power of -25 dBm.

The TL0374J-EVB-C is expected to show a gain of about 25.5 dB at 100 MHz; see Table 4 for the expected results.

Additional test equipment is required for a comprehensive evaluation. For noise figure, use a noise figure analyzer or a spectrum analyzer with noise option, together with a low excess noise ratio (ENR) noise source. For the third-order intercept point, use two signal generators, a high-isolation power combiner and a spectrum analyzer. For power compression and power handling, use a signal generator and a two-channel power meter; make sure that the driver amplifier has sufficient output power and that couplers and attenuators have adequate power handling.

Measurements taken at the SMA connectors include the losses of the connectors and the PCB. The through line on the board, consisting of an RF input and an RF output line of equal length, should be measured to calibrate these effects.

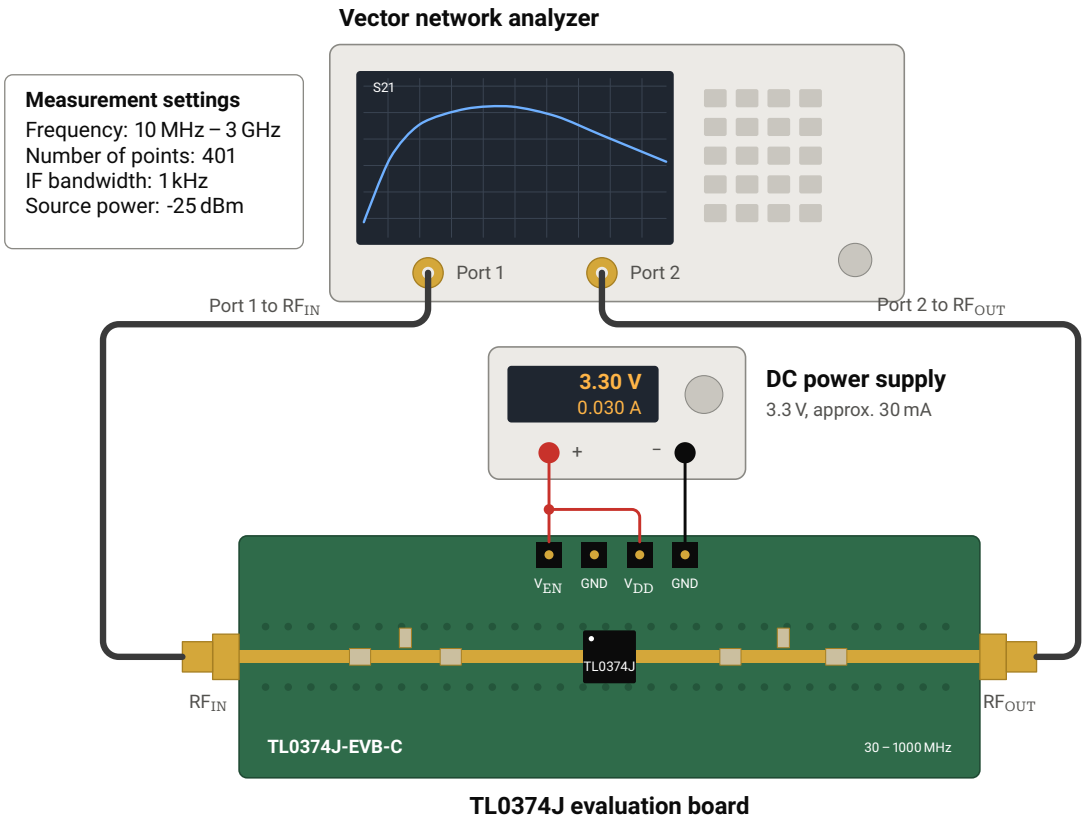



Figure 5: Test set-up for S-parameter measurements.


©2024-2026 TagoreTech Inc.

## TL0374J-EVB-C Measurement Summary

All measurements are carried out at room temperature, $V_{DD} = V_{enable} = 3.3\,V$, with the reference planes at the SMA connectors.

Table 4: TL0374J-EVB-C electrical characteristics summary, $T_A = +25°C$

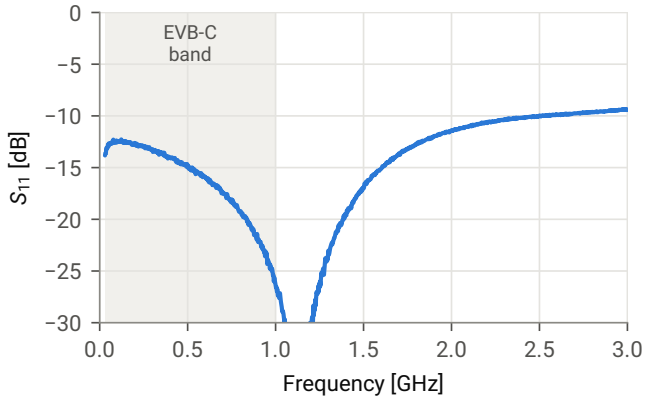
| Frequency [MHz] | Noise figure[1] [dB] | Gain [dB] | $OP_{1dB}$ [dBm] | OIP3[2] [dBm] | $S_{11}$ [dB] | $S_{22}$ [dB] | $\mu$ |
|---|---|---|---|---|---|---|---|
| 30 | 1.1 | 25.6 | 14.4 | 28.4 | -13.8 | -22.7 | 1.1 |
| 100 | 0.7 | 25.7 | 14.3 | 27.5 | -12.5 | -23.8 | 1.2 |
| 250 | 0.6 | 25.2 | 14.6 | 28.1 | -12.9 | -17.2 | 1.3 |
| 500 | 0.6 | 23.9 | 14.7 | 28.7 | -14.8 | -14.6 | 1.5 |
| 750 | 0.5 | 22.6 | 15.2 | 29.1 | -18.0 | -18.4 | 1.7 |
| 1000 | 0.5 | 21.3 | 14.9 | 29.3 | -26.3 | -28.4 | 1.7 |

[1] De-embedded: the losses of the input connector and trace are removed.
[2] Two tones, 1 MHz spacing, 0 dBm output power per tone.

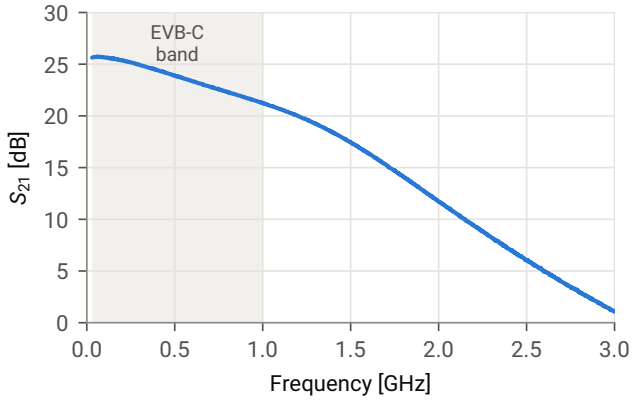
The S-parameters are shown from 30 MHz to 3 GHz; the shaded area marks the 30 – 1000 MHz band. The Smith charts show $S_{11}$ and $S_{22}$ normalised to 50 Ω, with the band as a thick line. The stability factors $\mu$ (load) and $\mu'$ (source) are calculated from the S-parameters; $\mu > 1$ indicates unconditional stability. The noise figure is de-embedded to the device pins. Gain and gain compression are measured with a single tone, the output third-order intercept point with two tones 1 MHz apart.


©2024-2026 TagoreTech Inc.

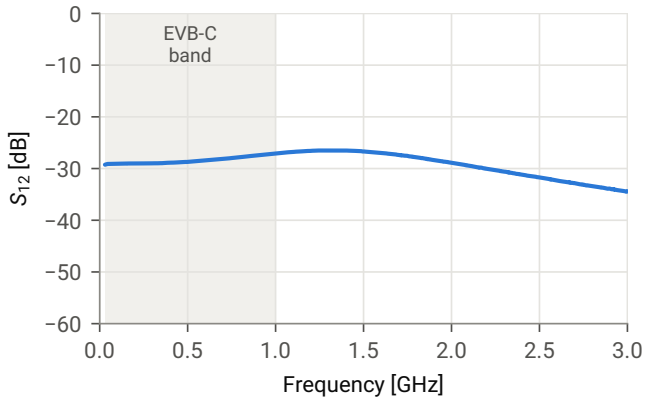
## TL0374J-EVB-C Measurement Results



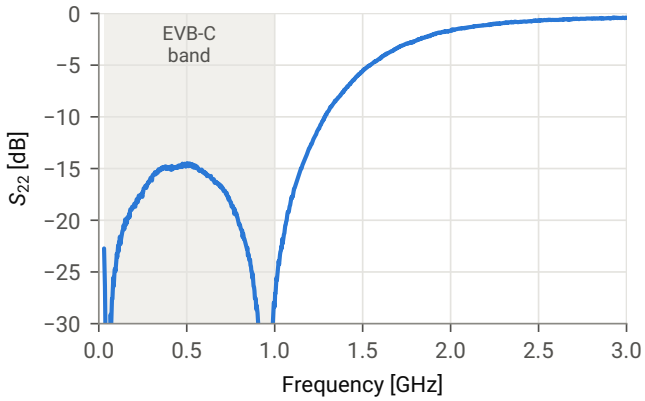

(a) $S_{11}$ (input return loss) vs. frequency.



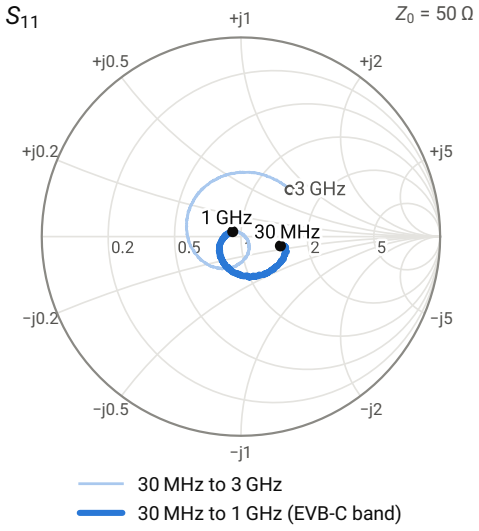

(b) $S_{21}$ (gain) vs. frequency.



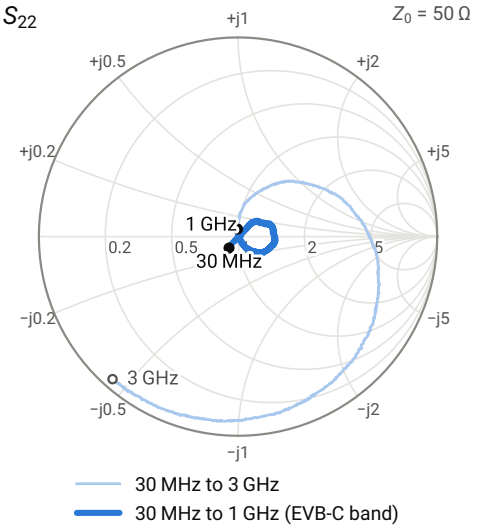

(c) $S_{12}$ (reverse isolation) vs. frequency.




(d) $S_{22}$ (output return loss) vs. frequency.




(e) $S_{11}$ Smith chart, input impedance.

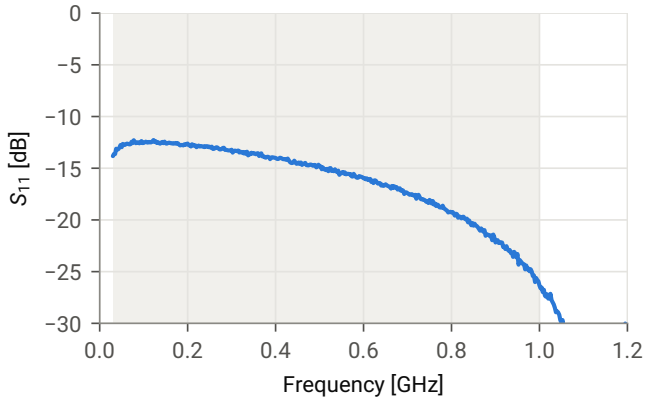



(f) $S_{22}$ Smith chart, output impedance.

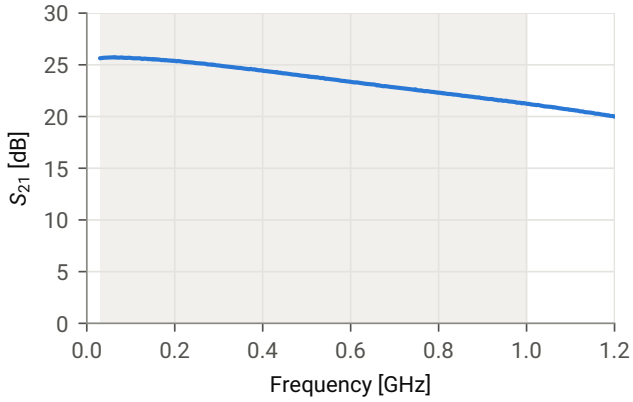
Figure 6: TL0374J-EVB-C S-parameters, 30 MHz to 3 GHz, $+25$°C, 3.3 V, 30 mA. Shaded and thick line: 30 – 1000 MHz band.


©2024-2026 TagoreTech Inc.

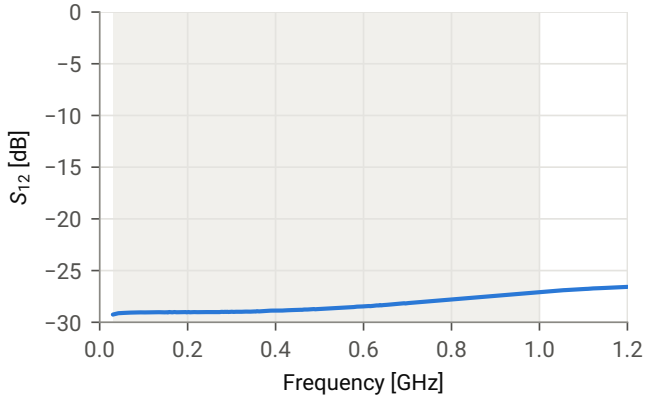
## S-parameters, 30 – 1000 MHz band



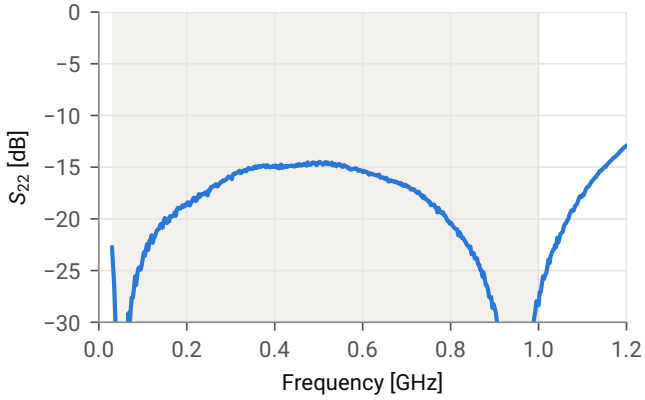

(a) $S_{11}$ (input return loss) vs. frequency.



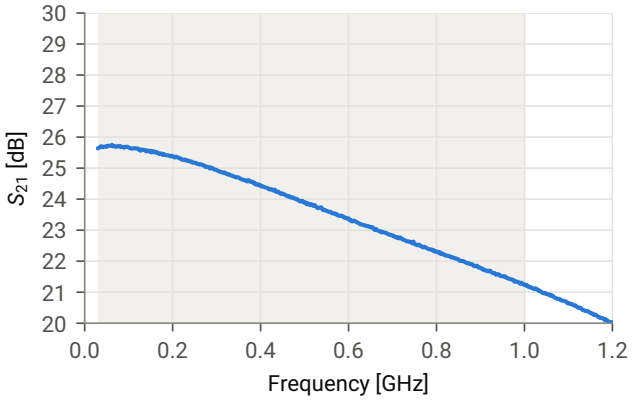

(b) $S_{21}$ (gain) vs. frequency.



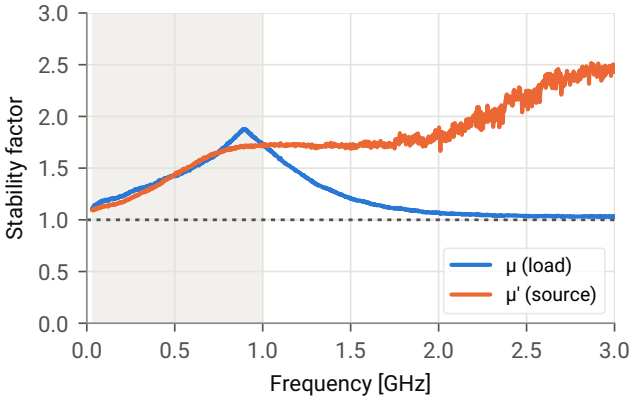

(c) $S_{12}$ (reverse isolation) vs. frequency.




(d) $S_{22}$ (output return loss) vs. frequency.




(e) $S_{21}$ (gain) vs. frequency, expanded scale.

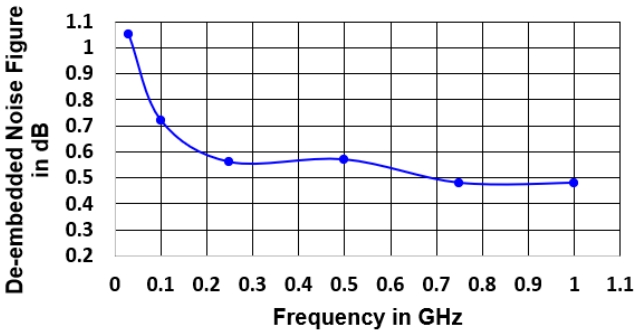



(f) Stability factors $\mu$ and $\mu'$ vs. frequency, 30 MHz to 3 GHz.

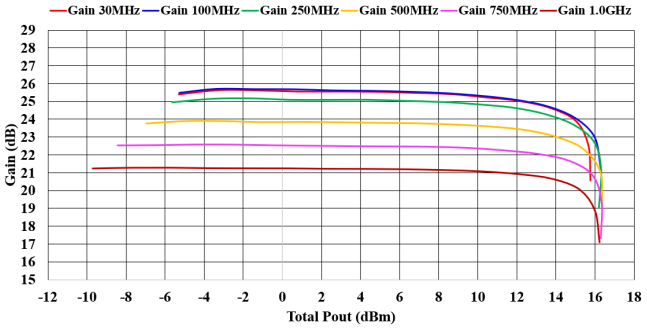
Figure 7: TL0374J-EVB-C S-parameters, 30 – 1000 MHz band, $+25$°C, 3.3 V, 30 mA.


©2024-2026 TagoreTech Inc.

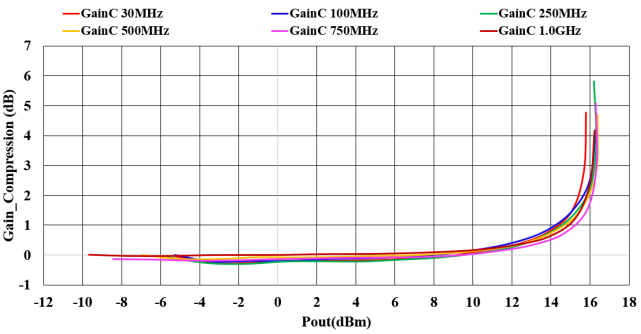
## Noise figure and large-signal performance



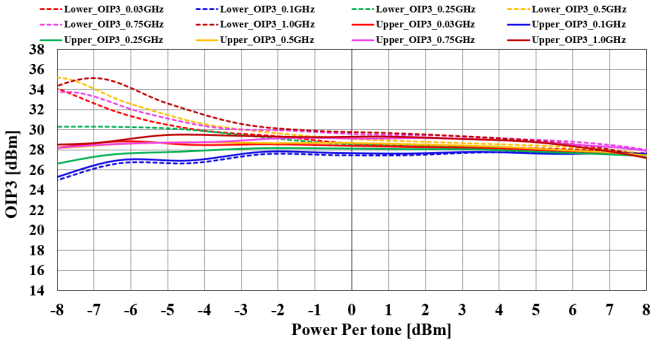

(a) De-embedded noise figure. The input trace loss is about 0.01 – 0.03 dB, so the SMA to SMA noise figure is 0.5 – 1.05 dB.




(b) Gain vs. output power.




(c) Gain compression vs. output power.




(d) OIP3 vs. output power per tone, 1 MHz tone spacing.

Figure 8: TL0374J-EVB-C noise figure and large-signal performance, $+25$°C, 3.3 V, 30 mA.

# Changelog

Table 5: Changelog

| Date | Revision | Notes |
|---|---|---|
| 07/30/2024 | 2.3 | Application note in the previous format. |
| 09/27/2026 | 3.0 | Application note converted to the common TagoreTech document template: schematic redrawn with not-assembled positions marked DNP, biasing sequence and test set-up added, S-parameters with Smith charts and stability factors re-drawn as vector graphics from the measured Touchstone data, links to the TL0374J datasheet and application notes TN-001/TN-002, QR code link to the latest version. |


©2024-2026 TagoreTech Inc.

# Legal Information

## Published by

TagoreTech Inc.
601 Campus Drive, Suite B3
Arlington Heights, IL 60004, USA

©2024-2026 All Rights Reserved

TagoreTech is a fabless US semiconductor company specializing in GaN RF solutions: high-power RF switches in unique GaN-on-Si technology, RF power amplifiers in GaN-on-SiC, and reconfigurable front-end solutions — such as tunable power amplifiers and antenna tuning — that combine the two. TagoreTech serves defense, radar, datalink, 5G infrastructure, and public safety markets.

## Legal Disclaimer

The information provided in this document is believed to be accurate and reliable, but shall in no event be regarded as a guarantee of conditions or characteristics. TagoreTech makes no warranties, express or implied, including without limitation any implied warranty of merchantability or fitness for a particular purpose. TagoreTech assumes no responsibility for the consequences of the use of this information, nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of TagoreTech. Customers are responsible for validating the suitability of TagoreTech products in their own applications and for compliance with all applicable laws and regulations, including export control requirements. Specifications are subject to change without notice. Errors and omissions excepted.

Use in critical applications. TagoreTech products are not designed or authorized for use in life-support devices or systems, safety-critical applications, or applications where failure could reasonably be expected to cause personal injury, unless expressly agreed in writing by an authorized representative of TagoreTech. Any such use is at the customer's own risk.

## Sales Contact

For further information on technology, delivery terms, conditions, and prices, please contact TagoreTech: support@tagoretech.com or contact our distributors or representatives as listed at TagoreTech website.
EVBs available from Tagore directly. Please click the blue links.

## Buy TL0374J directly

DigiKey Digikey TL0374J

RichardsonRFPD An Arrow Company RFPD TL0374J

## TL0374J Product information

Please visit TL0374J product page for further information TL0374J Product Page

Latest version of TL0374J datasheet is available for download: TL0374J Datasheet

Latest version of this application note is available for download: TL0374J-EVB-C Application Note

## TagoreTech Inc. Product Brochure and datasheets

Tagore Product Brochure and links to all available datasheets is available for download:
TagoreTech Product Brochure

Scan for the latest application note