

TSL8029N

Single Channel 2 – 5 GHz 100-Watt Receiver Front End for MACRO base station

Application Note: TSL8029N EVB B

Application Note 3300 MHz~4200 MHz 5 V, 90 mA [HG]

Rev-1.0



List of Contents

1	General Description
2	TSL8029N-EVB-B Board Details
3	TSL8029N-EVB-B Bill of Material
4	TSL8029N-EVB-B Biasing sequence
5	TSL8029N-EVB-B Board Measurement Summary
6	TSI 8029N-EVB-B Board Measurement Results



1. General Description

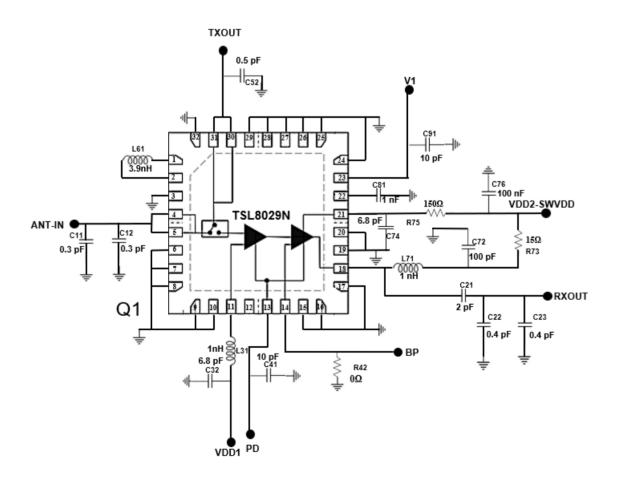
The TSL8029N is a single-channel, integrated RF, front-end, multichip module designed for different applications. The device operates from 2 GHz to 5 GHz. The TSL8029N is configured with a cascading, two-stage, GaAs LNA and a GaN based SPDT switch.

In high gain mode, the cascaded two-stage LNA and switch offer a low noise figure of 1.4 dB and a high gain of 33 dB at 3.6 GHz with an output third-order intercept point (OIP3) of 33 dBm (typical) at high gain mode. In low gain mode, one stage of the two-stage LNA is in bypass, providing 14.5 dB of gain at a lower current of 50 mA. In power-down mode, the LNAs are turned off and the device draws 4 mA.

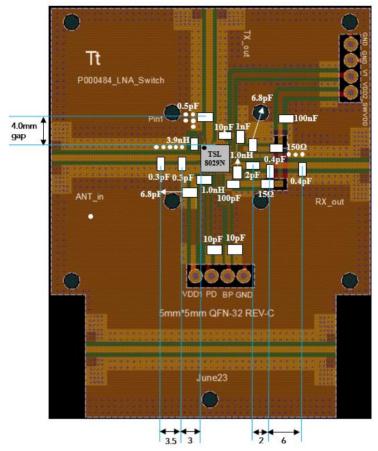
In transmit operation, when RF inputs are connected to a termination pin (TX), the switch provides low insertion loss of 0.5 dB at 3.6 GHz and handles long-term evolution (LTE) average power (8 dB peak to average ratio (PAR)) of 50 dBm for full lifetime operation.

The device comes in an RoHS compliant, compact, 5 mm \times 5 mm \times 0.85 mm, 32-lead QFN. TSL8029N EVB-B is tuned for 3.3-4.2 GHz.

2. TSL8029N-EVB-B Board Details







Note: Series cap on ANT and TX ports should have 250 V voltage ratings to handle 100 W power. The heatsink needs to be added at bottom of this board for proper power spreading.

Figure 2.1 TSL8029N-EVB-B 3300 MHz ~ 4200 MHz Schematic and EVB Layout

3. TSL8029N-EVB-B Bill of Material

Component ID	Value	Manufacturer	Recommended Part Number	Qty
C11, C12	0.3 pF	Murata 600S0R3BT250XT		2
C21	2 pF	Murata	GJM1555C1H2R0BB01D	1
C22, C23	0.4 pF	Murata GJM1555C1HR40BB01J		2
L31, L71	1 nH	Coil craft	0402DC-1N0XJRW	2
C32, C74	6.8 pF	Murata	GJM1555C1H6R8BB01D	2
C41, C91	10 pF	Murata	GJM1555C1H100JB01D	2
R42	0 Ω	Panasonic ERJ-2GE0R00X		1
C52	0.5 pF	Murata	600S0R5BT250XT	1
L61	3.9 nH	Coil craft 0402DC-3N9XGRW		1
C72	100 pF	AVX	04025A101JAT4A	1
R73	15 Ω	Panasonic	nic ERJ-H2RD15R0X	
R75	150 Ω	Panasonic	ERJ-2RHD1500X	1
C76	100 nF	TDK C1005X7R1H104K050BE		1
C81	1 nF	Murata 04025C102JAT2A		1
Q1	GaAs LNA + GaN Switch	Tagore Tech	TSL8029N	
PCB	Rogers RO4350B, 20 mils, 1 oz copper			

Table 3.1 TSL8029N-EVB-B BOM



4. TSL8029N-EVB-B Biasing Sequence

Turn ON Device	Turn OFF Device			
 Apply bias to the VDD2_SWVDD and VDD1=5 V test points. Apply bias to the V1 test point. Apply bias to the BP test points. Apply bias to the PD test point. Apply an RF input signal. 	 Turn RF power off. Turn off V1, BP and PD Turn off VDD2_SWVDD and VDD1=5 V test points. 			

Table 4.1 TSL8029N-EVB-B Bias and Sequencing

5. TSL8029N-EVB-B Board Measurement Summary

Frequency (MHz)	Mode	S21 (dB)	S11 (dB)	S22 (dB)	EVB Noise Figure(dB)	OP1dBm	OIP3dBm
3300	RX	32.4	-10.5	-7.5	1.5	19.8	31
3600	High	32.3	-11	-8.9	1.5	21	32
3900	Gain	30.8	-12.9	-16.8	1.5	19	33
4200		29.5	-14.3	-24.4	1.6	19.2	37
3300		-0.7	-16.9	-15.9			
3600	TX	-0.7	-20.3	-17.5			
3900		-0.7	-21	-21.3			
4200		-0.8	-21.7	-17			

Table 5.1 TSL8029N-EVB-B Electrical Characteristics Summary



6. TSL8029N-EVB-BTest Results

All the tests are carried out at room temperature.

6.1. S parameters



Figure 6.1.1. S parameters of TSL8029N-EVB-B RX-HG 5 V, 90 mA

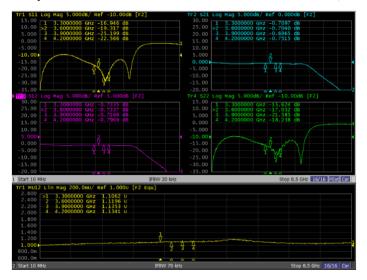


Figure 6.1.2. S parameters of TSL8029N-EVB-B TX



Figure 6.1.3. ANT to TX isolation of TSL8029N-EVB-B when RX-HG is on



6.2. SMA-SMA Noise Figure Test Results

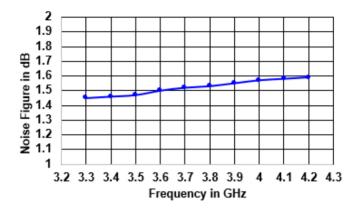
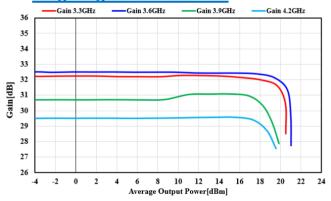


Figure 6.2.1. SMA-SMA Noise Figure of TSL8029N-EVB-B for 5 V, 90 mA for RX-HG mode

**Note: The trace loss is within the range of 0.2 dB, resulting in a de-embedded NF of 1.25 to 1.4 dB

6.3. Large Signal Test Results



GainC 3.3GHz — GainC 3.6GHz — GainC 3.9GHz — GainC 4.2GHz

5
4
4
2
4
-2 0 2 4 6 8 10 12 14 16 18 20 22 24

Average Output Power[dBm]

Figure 6.3.1. Gain vs P_{OUT} of TSL8029N-EVB-B for 5 V, 90 mA for RX-HG mode

Figure 6.3.2 Gain compression vs P_{OUT} of TSL8029N-EVB-B for 5 V, 90 mA for RX-HG mode

6.4. OIP3 Test Results

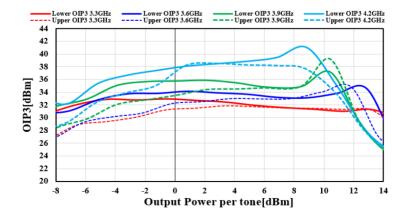


Figure 6.4.1 OIP3dBm vs P_{OUT} /tone of TSL8029N-EVB-B for 5 V, 90 mA for RX-HG mode



Edition Revision 2.0 - 2024-07-30

Published by

TagoreTech Inc.

601 W Campus Dr. Ste C1

Arlington Heights, IL 60004, USA

©2024 All Rights Reserved

Legal Disclaimer

The information provided in this document shall in no event be regarded as a guarantee of conditions or characteristics. TagoreTech assumes no responsibility for the consequences of the use of this information, nor for any infringement of patents or of other rights of third parties which may result from the use of this information. No license is granted by implication or otherwise under any patent or patent rights of TagoreTech. The specifications mentioned in this document are subject to change without notice.

Information

For further information on technology, delivery terms and conditions and prices, please contact TagoreTech: support@tagoretech.com.